DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



SYLLABUS BACHELOR OF TECHNOLOGY

in

ELECTRONICS ENGINEERING (VLSI DESIGN & TECHNOLOGY)

[Effective from Admitted batch 2024 and Admitted batch 2023 (3rd Semester) Onwards]



Vision, Mission and Objective of the Department

VISION

To achieve eminence in the field of quality techno-logical education and research in Communication, Remote Sensing, VLSI & Embedded System and other Electronics related fields.

MISSION

To develop into a department of excellence capable of producing competent techno-managers who can contribute effectively to the advancement of the society.

OBJECTIVES

- To prepare students to identify, formulate and solve real life and industrial problems with their knowledge of electronics and communication engineering.
- To develop techno-managerial ability to execute assigned responsibilities.
- To provide the guidance to develop moral and ethical attitudes.
- To provide education to adopt novel technologies towards the betterment of human life and society.



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FIRST YEAR B. TECH CURRICULUM 2024 (Common to all branches)

ter	F	IRST SEMESTER					SECO	OND SEMESTER				
Semester	Sub. Code	Subject Name	L	Т	P	С	Sub. Code	Subject Name	I	Т	P	С
	PH101A1	Engineering Physics	3	1	0	4	CH101A1	Engineering Chemistry	3	1	0	4
l st Yr	MA101A1	Engineering Mathematics—I/II	3	1	0	4	CS101A1	Computer Programming in C	3	1	0	4
	EC101A1	Basic Electronics	3	0	0	3	MA102A1	Engineering Mathematics–I/II	3	1	0	4
	CE101A1	Elements of Civil Engineering	3	0	0	3	EE101A1	Element of Electrical Engineering	3	0	0	3
	ME101A1	Engineering Graphics	2	0	0	2	ME102A1	Element of Mechanical Engineering	3	0	0	3
	ME101A4	Workshop Practice	0	0	1	1	CTTIONAL	Environmental Science*	1	0	0	1
	BA101A1	Communication Skills*	2	0	0	2	CS101A4	Computer Programming Lab	0	0	1	1
	BP101A1	Constitution of India	1	0	0	1	CHINIAA	Engineering Chemistry Lab	0	0	1	1
	PH101A4	Engineering Physics Lab	0	0	1	1		NCC (AU)	0	0	0	0
		NCC (AU)	0	0	0	0						
	Total Contac	t Hours (L + T + P)				21	Total Contac	t Hours (L + T + P)	_			21
	*Mandatory	audit course					*Mandatory	audit course				

Note: UHV-I has been introduced under Mandatory Induction Program.





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B.TECH. COURSE STRUCTURE – SEMESTER WISE

Year	Course	THIRD SE Course Title	distribution of the state of th	l Number	of Contact	Hours	Credits
	Code		Lecture (L)		Practica l (P)	Total Hours	Creares
	MA207A1	Engineering Mathematics III	3	1	0	4	4
	VT201A1	Electronic Devices and Components	3	1	0	4	4
	VT202A1	Digital Electronics and System Design	3	1	0	4	4
	VT203A1	Signals and Systems	3	1	0	4	4
2nd	VT2xxA3	Program Elective-I	3	1	0	4	4
	VT2xxA2	Open Elective-I/Minor/NCC	3	1	0	4	4
	VT201A4	Electronic Devices and Components Lab	0	0	1	2	1
	VT202A4	Digital Electronics and System Design Lab	0	0	1	2	- 1
10m + =	VT201A5	Project Based Learning-I	0	0	1	2	1
TOTAL	<i>.</i>		18	6	3	30	27

Year	Course	FOURTH S Course Title	THE RESERVE OF THE PARTY OF	l Number	of Contact	Hours	Credits
	Code		Lecture (L)	Tutorial (T)	Practical (P)		Credits
	MA208A1	Engineering Mathematics IV	3	1	0	4	4
	VT204A1	Analog Electronic Circuits	3	1	0	4	4
	VT205A1	Microprocessor, Microcontroller, and ARM processor	3	1	0	4	4
	VT2xxA2	Open Elective-II/Minor/NCC	3	1	0	4	4
2nd	VT2xxA3	Program Elective-II	3	1	0	4	4
	GN201A1	Universal human values-II: understanding Harmony and ethical human conduct	3	0	0	3	3
	VT203A4	Analog Electronic Circuits Lab	0	0	1	2	1
	VT204A4	Microprocessor and microcontroller LAB	0	0	1	2	1
mom / ×	VT202A5	Project Based Learning-II	0	0	1	2	1
TOTAL	- 0	WANIPALO	18	5	3	29	26
Simen of Election	SMIT SMIT	On English Carry Carry	Si	Q.		Page 9 of	

		FIFTH SEN	MESTER				
Year	Course Code	Course Title	Tota		of Contact		Credits
			Lecture (L)	Tutorial (T)	Practical (P)	Total Hours	
	VT301A1	Communication Engineering	3	1	0	4	4
	VT302A1	Introduction to Microfabrication	3	1	0	4	4
	VT303A1	VLSI Verification and Testing using EDA tools	3	1	0	4	4
		Embedded System	3	1	0	4	- 4
3rd	VT3xxA3	Program Elective-III/MOOC	3	3)	0	4	. 3
	VT3xxA2	Open Elective-III/Minor/NCC	3	1	0	4	- 4
	VT301A4	HDL simulation LAB	0	0	1	2	1
ĺ	VT302A4	Python Scripting Lab	0	0	1	2	1
İ	VT301A5	Project Based Learning-III	0	0	1	2	1
		Quantitative aptitude and logical reasoning	1	0	0	1	- 1
İ	V/T20140	Industrial Training-I	0	0	1	-	1
OTAL			18	6	4	30*	- 28







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Year	Course Code	Course Title		al Number	of Contact	Hours	Credits
			Lecture (L)	Tutorial (T)	Practical (P)	Total Hours	
	BA346A1	Industrial Management	2	0	0	2	2
	VT305A1	Semiconductor Materials Synthesis and Characterization	3	1	0	4	4
	VT306A1	Micro Electronics and VLSI Design)	3	1	0	4	4
	VT3xxA3	Program Elective-IV	3	1	0	4	4
	VT3xxA3	Program Elective-V	3	1	0	4	4
3rd	VT3xxA2	Open Elective-IV/Minor/NCC	3	1	0	4	4
	VT303A4	Communication Lab	0	0	1	2	1
	VT304A4	ARM based SoC Design Lab {Foundry Familiarization workshop / MOOC virtual Lab}	0	0	1	2	1
	GN302A1	Quantitative aptitude and logical reasoning	1	0	0	1	1
mom.	VT302A5	Mini Project	0	0	1	1	1
TOTAL	r.		17	5	4	28	26

^{*}Audit course

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Year	Course	Course Title	Total	Number o	f Contact H	lours	Credits
	Code		Lecture (L)	Tutorial (T)	Practical (P)	Total Hours	Creans
	VT4xxA2	Open Elective-V/Minor/NCC	3	1	0	4	4
4 th	VT4xxA2	Choice Based Elective (Open Elective)	3	0	0	3	3
4	VT401A6	Research based Project / Industrial Project -Phase-I	0	0	7	2	7
	VT401A9	Industrial Training-II	0	0	1	(A.=.).	1
TOTAL			6	1	1	9*	15
	建图整理	EIGHT SE	MESTER	MONEY EN	SA RATE OF SE	re that	13
Year	Course	Course Title	Total	Number of	f Contact H	ours	Credits
	Code		Lecture	Tutorial	Practical	Total	Creatis
			(L)	(T)	(P)	Hours	
4 th	VT4xxA2	Open Elective-VI/Minor/NCC	3	1	0	4	4
	VT402A6	Major Project work	0	0	12	_	12
OTAL			3		12		



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	STEEL STEEL	LIST OF ELECTIVES FOR 3 ^{rd.} SEMESTER	
		PROGRAM ELECTIVE-1	
Year	COURSE CODE	COURSE TITLE	CREDIT
	VT201A3	Electromagnetic Theory	4
	VT202A3	Network Analysis and Synthesis	4
2nd	VT203A3	OOPs with C++	4
_,,,	VT204A3	Internet of Things	4
	VT205A3	Computer Organization and Architecture	4

A STATE OF S		LIST OF ELECTIVES FOR 4 TH SEMESTER	第一条外部第一条
		PROGRAM ELECTIVE-2	
Year	COURSE CODE	COURSE TITLE	CREDIT
je	VT206A3	Computer Networks	4
2nd	VT207A3	Electronic Instrumention and Measurements	4
	VT208A3	Data Structure	4
	VT209A3	Advanced Electronic Devices	4

	LIS	F OF ELECTIVES FOR 5 TH SEMESTEI PROGRAM ELECTIVE-3	
Year	COURSE CODE	COURSE TITLE	CREDIT
	VT301A3	FPGA Architecture	4
	VT302A3	Digital Signal Processing	4
3rd	VT303A3	VLSI Physical Design	4
	VT304A3	JAVA	4
	VT305A3	Database Management System	4



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	L	IST OF ELECTIVES FOR 6 TH SEMESTER	
		PROGRAM ELECTIVE-4	
Year	COURSE CODE	COURSE TITLE	CREDIT
	VT306A3	Advanced Semiconductor Devices and Flexible Electronics	4
	VT307A3	Linear and Digital Control Systems	4
	VT308A3	Information Theory and Coding	4
	VT309A3	MEMS and NEMS	4
		PROGRAM ELECTIVE-5	
3rd	VT310A3	Advanced VLSI and SoC Design	4
	VT311A3	Semiconductor Device Modelling	4
	VT312A3	Machine Learning	4

MESTER	COURSE	OPEN ELECTIVE/MINOR COURSE TITLE	CONDUCTO
MESTER	CODE	COURSE TITLE	CREDIT
	VT201A2	Introduction to Complex Variables	4
3rd	VT202A2	Computer Organization and Architecture	4
	VT203A2	Python Programming	4
	VT204A2	Mathematics for Communication Engineering	
	VT205A2	Signal Processing for Communication	4
4th	VT206A2	Semiconductor Devices and Circuits	4
	VT207A2	Sensors and Actuators	4
	VT208A2	Advanced Antenna Design	
	VT301A2	Optimization Technique	4
5th	VT302A2	Solid State Devices	4
_	VT303A2	IOT Gateways and Edge Computing	4
	VT304A2	Advanced Digital Communication	
	VT305A2	Pattern Recognition	4
	VT306A2	Nano Electronic Devices and Materials	4

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6th	VT307A2	Communication Pathways between Cloud and IoT	4
	VT308A2	Modern Wireless Communication System	
	VT401A2	Time Frequency Analysis	4
	VT402A2	Advanced VLSI Design and Applications	4
7th	VT403A2	Artificial Intelligence and Machine Learning	4
	VT404A2	Coding for MIMO Communication	
	VT405A2	Advanced Digital Signal Processing	4
8th	VT406A2	ASIC Design	4
1170/PDE	VT407A2	Data Centre and Cloud Computing	4
	VT408A2	5G Mobile Communication	

SEMESTER	COURSE CODE	COURSE TITLE	CREDIT
7th	VT4xxA2	Any subjects related to foreign language or Indian language.	3





